


**UNIVERSITY COLLEGE OF ENGINEERING VIZIANAGARAM  
JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA**

**M.Tech I Semester (R16) Regular – 2016 Admitted Batch Examinations - Dec/Jan, 2016/17  
REVISED TIMETABLE**

Time:10.00 AM to 01:00 PM

Branch Specialization Date / Day	EEE (Advanced Power System)	ME (Machine Design)	ECE (Systems and Signal processing)	CSE (Computer Science & Enggg.)
28.12.2016 (Wednesday)	<b>Elective -II</b> Programmable Logic Controllers & Applications (GI5613)	Advanced Mechanics of Solids (I1501)	Digital Data Communications(I4504)	Mathematical Foundations of Computer Science (I5801)
31.12.2016 (Saturday)	Reactive Power Compensation & Management (GI5604)	Advanced Mechanisms (I1502)	Advanced Digital Signal processing (I4503)	Computer Organization and Architecture (I5802)
02.01.2017 (Monday)	Power System Operation and Control (GI5603)	<b>Elective - I</b> Geometric Modeling (I1506)	--	Data Base Management Systems (I5803)
04.01.2017 (Wednesday)	<b>Elective -I</b> Electrical Distribution Systems (GI5605)	Computational Methods in Engineering (I1801)	--	Advanced Operating Systems (I0504)
06.01.2017 (Friday)	Microprocessors & Microcontrollers (GI5601)	<b>Elective - II</b> Mechanics (I1508)	<b>Elective -I</b> Statistical Signal Processing(I3802)	Data warehousing and Data Mining (I0505)
09.01.2017 (Monday)	HVDC Transmission (GI5602)	Mechanical Vibrations (I1503)	--	--
11.01.2017 (Wednesday)	--	--	Transform Techniques (I4502)	--
13.01.2017 (Friday)	--	--	<b>Elective -II</b> Soft Computing Techniques(I6807)	--

  
**VICE-PRINCIPAL**