

Revaluation Results of III B.Tech [R13 & R10] I Semester [VV] Regular/Supplementary Examination NOV-2017

Sino	CC	HTNO	SUBCODE	SUBNAME	INT	EXT	CREDITS
1	VV	15VV1A0511	RT31051	COMPILER DESIGN	22	39	3
2	VV	15VV1A0516	RT31051	COMPILER DESIGN	23	46	3
3	VV	15VV1A0417	RT31043	CONTROL SYSTEMS	27	53	3
4	VV	15VV1A0516	RT31054	DATABASE MANAGEMENT SYSTEMS	27	54	3
5	VV	15VV1A0405	RT31044	DIGITAL SYSTEM DESIGN & DIGITAL IC APPLICATION	18	27	3
6	VV	15VV1A0417	RT31044	DIGITAL SYSTEM DESIGN & DIGITAL IC APPLICATION	22	46	3
7	VV	15VV1A0421	RT31044	DIGITAL SYSTEM DESIGN & DIGITAL IC APPLICATION	23	36	3
8	VV	15VV1A0437	RT31044	DIGITAL SYSTEM DESIGN & DIGITAL IC APPLICATION	20	48	3
9	VV	15VV1A0441	RT31044	DIGITAL SYSTEM DESIGN & DIGITAL IC APPLICATION	22	43	3
10	VV	15VV1A0443	RT31044	DIGITAL SYSTEM DESIGN & DIGITAL IC APPLICATION	19	26	3
11	VV	15VV1A0444	RT31044	DIGITAL SYSTEM DESIGN & DIGITAL IC APPLICATION	19	26	3
12	VV	15VV1A0445	RT31044	DIGITAL SYSTEM DESIGN & DIGITAL IC APPLICATION	19	46	3
13	VV	15VV1A3106	MT314	FOUNDRY TECHNOLOGY	28	45	3
14	VV	15VV1A3121	MT315	IRON PRODUCTION	20	45	3
15	VV	15VV1A3119	MT316	NON FERROUS EXTRACTIVE METALLURGY	22	32	3
16	VV	15VV1A0516	RT31053	PRINCIPLES OF PROGRAMMING LANGUAGES	28	49	3
17	VV	15VV1A0410	RT31041	PULSE & DIGITAL CIRCUITS	27	36	3
18	VV	15VV1A0445	RT31041	PULSE & DIGITAL CIRCUITS	24	34	3

DATE: 07-04-2018

N. Mohan Rao
CONTROLLER OF EXAMINATIONS