

UNIVERSITY COLLEGE OF ENGINEERING VIZIANAGARAM
JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

II B.Tech II Semester (R10) Supplementary Examination November - 2017

T I M E T A B L E

T I M E : 02.00 PM TO 05-00 PM

BRANCH	DATE AND DAY					
	14.11.2017 (Tuesday)	15.11.2017 (Wednesday)	16.11.2017 (Thursday)	17.11.2017 (Friday)	18.11.2017 (Saturday)	20.11.2017 (Monday)
ELECTRICAL AND ELECTRONICS ENGINEERING	Electrical Circuit Analysis -II	Electrical Machines -II	Pulse & Digital Circuits	Power Systems-I	Switching theory & logic Design	Control Systems
MECHANICAL ENGINEERING	Mechanics of Solids	Kinematics of Machinery	Thermal Engineering -I	Production Technology	Metallurgy & Materials Science	Machine Drawing
ELECTRONICS AND COMMUNICATION ENGINEERING	Electronics Circuit Analysis	Analog Communications	Pulse & Digital Circuits	EM Waves and Transmission Lines	Switching Theory & Logic Design	Control Systems
COMPUTER SCIENCE & ENGINEERING	Software Engineering	Object Oriented Programming through Java	Data Base Management Systems	Computer Organization	Principal of Programming Language	Formal Languages and Automata Theory
INFORMATION TECHNOLOGY	Data Communication	Object Oriented Programming through Java	Data Base Management Systems	Computer Organization & Architecture	Principal of Programming Language	Automata Theory & Compiler Design
METALLURGICAL ENGINEERING	Mechanics of Fluids	Instrumentation	Metallurgical Analysis	Fuels Refectories and Furnaces	Electrical & Elecgronics Engineering	Metallurgical Thermo-dynamics