### UNIVERSITY COLLEGE OF ENGINEERING VIZIANAGARA
JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA


**I MID EXAMINATION March - 2017**

<table>
<thead>
<tr>
<th>Specialization</th>
<th>13-03-17</th>
<th>14-03-17</th>
<th>15-03-17</th>
<th>16-03-17</th>
<th>17-03-17</th>
<th>20-03-17</th>
</tr>
</thead>
<tbody>
<tr>
<td>M.Tech (Advanced Power System)</td>
<td>PSDS</td>
<td>FACTS</td>
<td>RTCPS</td>
<td>APSP</td>
<td>PQ</td>
<td>PSD</td>
</tr>
<tr>
<td>M.Tech (Machine Design)</td>
<td>O&amp;R</td>
<td>ESA</td>
<td>FEM</td>
<td>DAM</td>
<td>SACM</td>
<td>Mechatronics</td>
</tr>
<tr>
<td>M.Tech (Systems and Signal processing)</td>
<td>ASP</td>
<td>I&amp;VP</td>
<td>DET</td>
<td>DSPA</td>
<td>ESD</td>
<td>WC&amp;N</td>
</tr>
<tr>
<td>M.Tech (Computer Science &amp; Engg.)</td>
<td>CS</td>
<td>CN</td>
<td>BIGDA</td>
<td>AUP</td>
<td>SE</td>
<td>CC</td>
</tr>
</tbody>
</table>

**Timing: 10.00 AM To 12.00 Noon**

<table>
<thead>
<tr>
<th>Specialty</th>
<th>Course</th>
</tr>
</thead>
<tbody>
<tr>
<td>PSDS</td>
<td>Power System Dynamics Stability</td>
</tr>
<tr>
<td>FACTS</td>
<td>Flexible AC Transmission Systems</td>
</tr>
<tr>
<td>RTCPS</td>
<td>Real Time Control of Power System</td>
</tr>
<tr>
<td>APSP</td>
<td>Advanced Power System Protection</td>
</tr>
<tr>
<td>PQ</td>
<td>Power Quality</td>
</tr>
<tr>
<td>PSD</td>
<td>Power System Deregulation</td>
</tr>
<tr>
<td>O&amp;R</td>
<td>Optimization and Reliability</td>
</tr>
<tr>
<td>ESA</td>
<td>Experimental Stress Analysis</td>
</tr>
<tr>
<td>FEM</td>
<td>Finite Element Methods</td>
</tr>
<tr>
<td>DAM</td>
<td>Design with advanced Materials</td>
</tr>
<tr>
<td>SACM</td>
<td>Signal Analysis and Condition Monitoring</td>
</tr>
<tr>
<td>Mechatronics</td>
<td>Mechatronics</td>
</tr>
<tr>
<td>ASP</td>
<td>Adaptive Signal Processing</td>
</tr>
<tr>
<td>I&amp;VP</td>
<td>Image &amp; Video Processing</td>
</tr>
<tr>
<td>DET</td>
<td>Detection and Estimation Theory</td>
</tr>
<tr>
<td>DSPA</td>
<td>DSP Processors and Architectures</td>
</tr>
<tr>
<td>ESD</td>
<td>Embedded system Design</td>
</tr>
<tr>
<td>WC&amp;N</td>
<td>Wireless Communication &amp; Networks</td>
</tr>
<tr>
<td>CS</td>
<td>Cyber Security</td>
</tr>
<tr>
<td>CN</td>
<td>Computer Networks</td>
</tr>
<tr>
<td>BIGDA</td>
<td>Big Data Analytics</td>
</tr>
<tr>
<td>AUP</td>
<td>Advanced UNIX Programming</td>
</tr>
<tr>
<td>SE</td>
<td>Software Engineering</td>
</tr>
<tr>
<td>CC</td>
<td>Cloud Computing</td>
</tr>
</tbody>
</table>

To
All HODs,
Copy to All Notice Boards.

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**VICE-PRINCIPAL**

*Vice-Principal (Academics)*

JNTUK University College of Engineering

Vizianagaram