

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

Results for MTECH II SEMESTER(R13) Regular/Supply Examinations July- 2016(Only for vv college.)

SNO	CC	HTNO	SUBCODE	SUBNAME	IMF	HMFIM	CREDITS
1	VV	13VV1D1501	H2103	FINITE ELEMENT METHOD	16	36	1
2	VV	13VV1D1506	H2103	FINITE ELEMENT METHOD	16	36	1
3	VV	13VV1D1512	H2103	FINITE ELEMENT METHOD	14	34	0
4	VV	13VV1D1513	H2103	FINITE ELEMENT METHOD	10	26	0
5	VV	13VV1D5817	H0501	DATA WAREHOUSING AND DATA MINING	14	-1	0
6	VV	13VV1D5824	H2508	CLOUD COMPUTING	15	39	1
7	VV	14VV1D1502	H1501	OPTIMIZATION AND RELIABILITY	34	28	1
8	VV	14VV1D1506	H2103	FINITE ELEMENT METHOD	16	42	1
9	VV	14VV1D1508	H1501	OPTIMIZATION AND RELIABILITY	30	15	0
10	VV	14VV1D1511	H2103	FINITE ELEMENT METHOD	23	26	0
11	VV	15VV1D1501	H1501	OPTIMIZATION AND RELIABILITY	38	36	1
12	VV	15VV1D1501	H1502	EXPERIMENTAL STRESS ANALYSIS	39	49	1
13	VV	15VV1D1501	H1505	DESIGN FOR MANUFACTURING ELECTIVE-II	37	48	1
14	VV	15VV1D1501	H1507	TRIBOLOGY ELECTIVE-III	39	46	1
15	VV	15VV1D1501	H1512	MECHATRONICS ELECTIVE-IV	34	37	1
16	VV	15VV1D1501	H1514	DESIGN PRACTICE LAB	38	58	1
17	VV	15VV1D1501	H2103	FINITE ELEMENT METHOD	39	46	1
18	VV	15VV1D1502	H1501	OPTIMIZATION AND RELIABILITY	36	-1	0
19	VV	15VV1D1502	H1502	EXPERIMENTAL STRESS ANALYSIS	38	-1	0
20	VV	15VV1D1502	H1505	DESIGN FOR MANUFACTURING ELECTIVE-II	19	-1	0
21	VV	15VV1D1502	H1507	TRIBOLOGY ELECTIVE-III	19	-1	0
22	VV	15VV1D1502	H1512	MECHATRONICS ELECTIVE-IV	17	-1	0
23	VV	15VV1D1502	H1514	DESIGN PRACTICE LAB	38	-1	0
24	VV	15VV1D1502	H2103	FINITE ELEMENT METHOD	30	-1	0
25	VV	15VV1D1503	H1501	OPTIMIZATION AND RELIABILITY	36	27	1
26	VV	15VV1D1503	H1502	EXPERIMENTAL STRESS ANALYSIS	39	37	1
27	VV	15VV1D1503	H1505	DESIGN FOR MANUFACTURING ELECTIVE-II	35	45	1
28	VV	15VV1D1503	H1507	TRIBOLOGY ELECTIVE-III	34	35	1
29	VV	15VV1D1503	H1512	MECHATRONICS ELECTIVE-IV	28	41	1
30	VV	15VV1D1503	H1514	DESIGN PRACTICE LAB	37	55	1

31	VV	15VV1D1503	H2103	FINITE ELEMENT METHOD	26	34	1
32	VV	15VV1D1504	H1501	OPTIMIZATION AND RELIABILITY	38	48	1
33	VV	15VV1D1504	H1502	EXPERIMENTAL STRESS ANALYSIS	39	42	1
34	VV	15VV1D1504	H1505	DESIGN FOR MANUFACTURING ELECTIVE-II	39	40	1
35	VV	15VV1D1504	H1507	TRIBOLOGY ELECTIVE-III	37	42	1
36	VV	15VV1D1504	H1512	MECHATRONICS ELECTIVE-IV	35	39	1
37	VV	15VV1D1504	H1514	DESIGN PRACTICE LAB	38	57	1
38	VV	15VV1D1504	H2103	FINITE ELEMENT METHOD	38	38	1
39	VV	15VV1D1505	H1501	OPTIMIZATION AND RELIABILITY	37	24	1
40	VV	15VV1D1505	H1502	EXPERIMENTAL STRESS ANALYSIS	38	42	1
41	VV	15VV1D1505	H1505	DESIGN FOR MANUFACTURING ELECTIVE-II	37	35	1
42	VV	15VV1D1505	H1507	TRIBOLOGY ELECTIVE-III	37	39	1
43	VV	15VV1D1505	H1512	MECHATRONICS ELECTIVE-IV	34	42	1
44	VV	15VV1D1505	H1514	DESIGN PRACTICE LAB	36	55	1
45	VV	15VV1D1505	H2103	FINITE ELEMENT METHOD	38	38	1
46	VV	15VV1D1506	H1501	OPTIMIZATION AND RELIABILITY	37	28	1
47	VV	15VV1D1506	H1502	EXPERIMENTAL STRESS ANALYSIS	36	33	1
48	VV	15VV1D1506	H1505	DESIGN FOR MANUFACTURING ELECTIVE-II	38	33	1
49	VV	15VV1D1506	H1507	TRIBOLOGY ELECTIVE-III	36	31	1
50	VV	15VV1D1506	H1512	MECHATRONICS ELECTIVE-IV	29	41	1
51	VV	15VV1D1506	H1514	DESIGN PRACTICE LAB	36	53	1
52	VV	15VV1D1506	H2103	FINITE ELEMENT METHOD	36	46	1
53	VV	15VV1D1509	H1501	OPTIMIZATION AND RELIABILITY	35	43	1
54	VV	15VV1D1509	H1502	EXPERIMENTAL STRESS ANALYSIS	36	36	1
55	VV	15VV1D1509	H1505	DESIGN FOR MANUFACTURING ELECTIVE-II	37	31	1
56	VV	15VV1D1509	H1507	TRIBOLOGY ELECTIVE-III	36	43	1
57	VV	15VV1D1509	H1512	MECHATRONICS ELECTIVE-IV	27	44	1
58	VV	15VV1D1509	H1514	DESIGN PRACTICE LAB	36	53	1
59	VV	15VV1D1509	H2103	FINITE ELEMENT METHOD	32	44	1
60	VV	15VV1D1510	H1501	OPTIMIZATION AND RELIABILITY	37	31	1
61	VV	15VV1D1510	H1502	EXPERIMENTAL STRESS ANALYSIS	38	37	1
62	VV	15VV1D1510	H1505	DESIGN FOR MANUFACTURING ELECTIVE-II	38	36	1
63	VV	15VV1D1510	H1507	TRIBOLOGY ELECTIVE-III	37	37	1
64	VV	15VV1D1510	H1512	MECHATRONICS ELECTIVE-IV	32	43	1

65	VV	15VV1D1510	H1514	DESIGN PRACTICE LAB	38	58	1
66	VV	15VV1D1510	H2103	FINITE ELEMENT METHOD	36	42	1
67	VV	15VV1D1511	H1501	OPTIMIZATION AND RELIABILITY	38	48	1
68	VV	15VV1D1511	H1502	EXPERIMENTAL STRESS ANALYSIS	38	34	1
69	VV	15VV1D1511	H1505	DESIGN FOR MANUFACTURING ELECTIVE-II	38	40	1
70	VV	15VV1D1511	H1507	TRIBOLOGY ELECTIVE-III	38	37	1
71	VV	15VV1D1511	H1512	MECHATRONICS ELECTIVE-IV	34	38	1
72	VV	15VV1D1511	H1514	DESIGN PRACTICE LAB	38	57	1
73	VV	15VV1D1511	H2103	FINITE ELEMENT METHOD	29	48	1
74	VV	15VV1D1512	H1501	OPTIMIZATION AND RELIABILITY	38	30	1
75	VV	15VV1D1512	H1502	EXPERIMENTAL STRESS ANALYSIS	38	39	1
76	VV	15VV1D1512	H1505	DESIGN FOR MANUFACTURING ELECTIVE-II	39	36	1
77	VV	15VV1D1512	H1507	TRIBOLOGY ELECTIVE-III	35	34	1
78	VV	15VV1D1512	H1512	MECHATRONICS ELECTIVE-IV	31	43	1
79	VV	15VV1D1512	H1514	DESIGN PRACTICE LAB	37	56	1
80	VV	15VV1D1512	H2103	FINITE ELEMENT METHOD	34	50	1
81	VV	15VV1D1514	H1501	OPTIMIZATION AND RELIABILITY	38	30	1
82	VV	15VV1D1514	H1502	EXPERIMENTAL STRESS ANALYSIS	37	32	1
83	VV	15VV1D1514	H1505	DESIGN FOR MANUFACTURING ELECTIVE-II	38	38	1
84	VV	15VV1D1514	H1507	TRIBOLOGY ELECTIVE-III	35	39	1
85	VV	15VV1D1514	H1512	MECHATRONICS ELECTIVE-IV	33	36	1
86	VV	15VV1D1514	H1514	DESIGN PRACTICE LAB	37	55	1
87	VV	15VV1D1514	H2103	FINITE ELEMENT METHOD	37	30	1
88	VV	15VV1D1515	H1501	OPTIMIZATION AND RELIABILITY	38	42	1
89	VV	15VV1D1515	H1502	EXPERIMENTAL STRESS ANALYSIS	39	42	1
90	VV	15VV1D1515	H1505	DESIGN FOR MANUFACTURING ELECTIVE-II	39	37	1
91	VV	15VV1D1515	H1507	TRIBOLOGY ELECTIVE-III	35	42	1
92	VV	15VV1D1515	H1512	MECHATRONICS ELECTIVE-IV	34	40	1
93	VV	15VV1D1515	H1514	DESIGN PRACTICE LAB	38	58	1
94	VV	15VV1D1515	H2103	FINITE ELEMENT METHOD	38	48	1
95	VV	15VV1D1517	H1501	OPTIMIZATION AND RELIABILITY	37	31	1
96	VV	15VV1D1517	H1502	EXPERIMENTAL STRESS ANALYSIS	37	24	1
97	VV	15VV1D1517	H1505	DESIGN FOR MANUFACTURING ELECTIVE-II	34	26	1
98	VV	15VV1D1517	H1507	TRIBOLOGY ELECTIVE-III	33	32	1

99	VV	15VV1D1517	H1512	MECHATRONICS ELECTIVE-IV	26	32	1
100	VV	15VV1D1517	H1514	DESIGN PRACTICE LAB	37	55	1
101	VV	15VV1D1517	H2103	FINITE ELEMENT METHOD	25	36	1
102	VV	15VV1D1518	H1501	OPTIMIZATION AND RELIABILITY	37	33	1
103	VV	15VV1D1518	H1502	EXPERIMENTAL STRESS ANALYSIS	38	44	1
104	VV	15VV1D1518	H1505	DESIGN FOR MANUFACTURING ELECTIVE-II	38	36	1
105	VV	15VV1D1518	H1507	TRIBOLOGY ELECTIVE-III	36	35	1
106	VV	15VV1D1518	H1512	MECHATRONICS ELECTIVE-IV	33	39	1
107	VV	15VV1D1518	H1514	DESIGN PRACTICE LAB	38	58	1
108	VV	15VV1D1518	H2103	FINITE ELEMENT METHOD	33	48	1
109	VV	15VV1D1519	H1501	OPTIMIZATION AND RELIABILITY	38	37	1
110	VV	15VV1D1519	H1502	EXPERIMENTAL STRESS ANALYSIS	38	31	1
111	VV	15VV1D1519	H1505	DESIGN FOR MANUFACTURING ELECTIVE-II	34	35	1
112	VV	15VV1D1519	H1507	TRIBOLOGY ELECTIVE-III	34	39	1
113	VV	15VV1D1519	H1512	MECHATRONICS ELECTIVE-IV	25	35	1
114	VV	15VV1D1519	H1514	DESIGN PRACTICE LAB	37	57	1
115	VV	15VV1D1519	H2103	FINITE ELEMENT METHOD	33	44	1
116	VV	15VV1D1520	H1501	OPTIMIZATION AND RELIABILITY	37	35	1
117	VV	15VV1D1520	H1502	EXPERIMENTAL STRESS ANALYSIS	38	34	1
118	VV	15VV1D1520	H1505	DESIGN FOR MANUFACTURING ELECTIVE-II	38	33	1
119	VV	15VV1D1520	H1507	TRIBOLOGY ELECTIVE-III	37	36	1
120	VV	15VV1D1520	H1512	MECHATRONICS ELECTIVE-IV	32	37	1
121	VV	15VV1D1520	H1514	DESIGN PRACTICE LAB	37	56	1
122	VV	15VV1D1520	H2103	FINITE ELEMENT METHOD	38	38	1
123	VV	15VV1D4001	H2512	SOFT COMPUTING	32	43	1
124	VV	15VV1D4001	H4001	ADVANCED UNIX PROGRAMMING	37	39	1
125	VV	15VV1D4001	H4002	INFORMATION SECURITY	38	36	1
126	VV	15VV1D4001	H4005	UML & DESIGN PATTERNS ELECTIVE-I	36	42	1
127	VV	15VV1D4001	H4006	MACHINE LEARNING	38	44	1
128	VV	15VV1D4001	H4009	IMAGE PROCESSING & PATTERN RECOGNITION E	33	44	1
129	VV	15VV1D4001	H4011	IT LAB-2	36	52	1
130	VV	15VV1D4003	H2512	SOFT COMPUTING	29	34	1
131	VV	15VV1D4003	H4001	ADVANCED UNIX PROGRAMMING	19	40	1
132	VV	15VV1D4003	H4002	INFORMATION SECURITY	35	37	1

133	VV	15VV1D4003	H4005	UML & DESIGN PATTERNS ELECTIVE-I	34	33	1
134	VV	15VV1D4003	H4006	MACHINE LEARNING	32	37	1
135	VV	15VV1D4003	H4009	IMAGE PROCESSING & PATTERN RECOGNITION E	25	38	1
136	VV	15VV1D4003	H4011	IT LAB-2	34	51	1
137	VV	15VV1D4005	H2512	SOFT COMPUTING	32	36	1
138	VV	15VV1D4005	H4001	ADVANCED UNIX PROGRAMMING	36	38	1
139	VV	15VV1D4005	H4002	INFORMATION SECURITY	36	43	1
140	VV	15VV1D4005	H4005	UML & DESIGN PATTERNS ELECTIVE-I	32	37	1
141	VV	15VV1D4005	H4006	MACHINE LEARNING	35	39	1
142	VV	15VV1D4005	H4009	IMAGE PROCESSING & PATTERN RECOGNITION E	31	43	1
143	VV	15VV1D4005	H4011	IT LAB-2	37	53	1
144	VV	15VV1D4006	H2512	SOFT COMPUTING	30	35	1
145	VV	15VV1D4006	H4001	ADVANCED UNIX PROGRAMMING	35	34	1
146	VV	15VV1D4006	H4002	INFORMATION SECURITY	33	38	1
147	VV	15VV1D4006	H4005	UML & DESIGN PATTERNS ELECTIVE-I	34	34	1
148	VV	15VV1D4006	H4006	MACHINE LEARNING	30	38	1
149	VV	15VV1D4006	H4009	IMAGE PROCESSING & PATTERN RECOGNITION E	23	41	1
150	VV	15VV1D4006	H4011	IT LAB-2	36	52	1
151	VV	15VV1D4501	H4501	ADAPTIVE SIGNAL PROCESSING	16	-1	0
152	VV	15VV1D4501	H4502	IMAGE & VIDEO PROCESSING	20	-1	0
153	VV	15VV1D4501	H4503	WIRELESS COMMUNICATION & NETWORKS	17	-1	0
154	VV	15VV1D4501	H4507	BIO-MEDICAL SIGNAL PROCESSING ELECTIVE-III	20	-1	0
155	VV	15VV1D4501	H4509	RADAR SIGNAL PROCESSING ELECTIVE-IV	20	-1	0
156	VV	15VV1D4501	H4511	ADVANCED SIGNAL PROCESSING LAB	32	-1	0
157	VV	15VV1D4501	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURES	20	-1	0
158	VV	15VV1D4502	H4501	ADAPTIVE SIGNAL PROCESSING	35	38	1
159	VV	15VV1D4502	H4502	IMAGE & VIDEO PROCESSING	28	30	1
160	VV	15VV1D4502	H4503	WIRELESS COMMUNICATION & NETWORKS	28	44	1
161	VV	15VV1D4502	H4507	BIO-MEDICAL SIGNAL PROCESSING ELECTIVE-III	35	41	1
162	VV	15VV1D4502	H4509	RADAR SIGNAL PROCESSING ELECTIVE-IV	28	41	1
163	VV	15VV1D4502	H4511	ADVANCED SIGNAL PROCESSING LAB	35	52	1
164	VV	15VV1D4502	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURES	34	37	1
165	VV	15VV1D4504	H4501	ADAPTIVE SIGNAL PROCESSING	33	34	1
166	VV	15VV1D4504	H4502	IMAGE & VIDEO PROCESSING	25	25	1

167	VV	15VV1D4504	H4503	WIRELESS COMMUNICATION & NETWORKS	27	31	1
168	VV	15VV1D4504	H4507	BIO-MEDICAL SIGNAL PROCESSING ELECTIVE-III	37	38	1
169	VV	15VV1D4504	H4509	RADAR SIGNAL PROCESSING ELECTIVE-IV	28	42	1
170	VV	15VV1D4504	H4511	ADVANCED SIGNAL PROCESSING LAB	37	53	1
171	VV	15VV1D4504	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURES	34	44	1
172	VV	15VV1D4505	H4501	ADAPTIVE SIGNAL PROCESSING	30	26	1
173	VV	15VV1D4505	H4502	IMAGE & VIDEO PROCESSING	25	17	0
174	VV	15VV1D4505	H4503	WIRELESS COMMUNICATION & NETWORKS	28	34	1
175	VV	15VV1D4505	H4507	BIO-MEDICAL SIGNAL PROCESSING ELECTIVE-III	34	28	1
176	VV	15VV1D4505	H4509	RADAR SIGNAL PROCESSING ELECTIVE-IV	20	31	1
177	VV	15VV1D4505	H4511	ADVANCED SIGNAL PROCESSING LAB	35	46	1
178	VV	15VV1D4505	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURES	28	25	1
179	VV	15VV1D4506	H4501	ADAPTIVE SIGNAL PROCESSING	38	37	1
180	VV	15VV1D4506	H4502	IMAGE & VIDEO PROCESSING	30	28	1
181	VV	15VV1D4506	H4503	WIRELESS COMMUNICATION & NETWORKS	33	48	1
182	VV	15VV1D4506	H4507	BIO-MEDICAL SIGNAL PROCESSING ELECTIVE-III	36	46	1
183	VV	15VV1D4506	H4509	RADAR SIGNAL PROCESSING ELECTIVE-IV	29	42	1
184	VV	15VV1D4506	H4511	ADVANCED SIGNAL PROCESSING LAB	36	51	1
185	VV	15VV1D4506	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURES	34	50	1
186	VV	15VV1D4507	H4501	ADAPTIVE SIGNAL PROCESSING	33	41	1
187	VV	15VV1D4507	H4502	IMAGE & VIDEO PROCESSING	30	32	1
188	VV	15VV1D4507	H4503	WIRELESS COMMUNICATION & NETWORKS	35	42	1
189	VV	15VV1D4507	H4507	BIO-MEDICAL SIGNAL PROCESSING ELECTIVE-III	36	45	1
190	VV	15VV1D4507	H4509	RADAR SIGNAL PROCESSING ELECTIVE-IV	28	44	1
191	VV	15VV1D4507	H4511	ADVANCED SIGNAL PROCESSING LAB	34	53	1
192	VV	15VV1D4507	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURES	34	34	1
193	VV	15VV1D4508	H4501	ADAPTIVE SIGNAL PROCESSING	36	41	1
194	VV	15VV1D4508	H4502	IMAGE & VIDEO PROCESSING	31	29	1
195	VV	15VV1D4508	H4503	WIRELESS COMMUNICATION & NETWORKS	33	49	1
196	VV	15VV1D4508	H4507	BIO-MEDICAL SIGNAL PROCESSING ELECTIVE-III	34	35	1
197	VV	15VV1D4508	H4509	RADAR SIGNAL PROCESSING ELECTIVE-IV	29	45	1
198	VV	15VV1D4508	H4511	ADVANCED SIGNAL PROCESSING LAB	36	48	1
199	VV	15VV1D4508	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURES	36	45	1
200	VV	15VV1D4509	H4501	ADAPTIVE SIGNAL PROCESSING	34	38	1

201	VV	15VV1D4509	H4502	IMAGE & VIDEO PROCESSING	27	30	1
202	VV	15VV1D4509	H4503	WIRELESS COMMUNICATION & NETWORKS	21	45	1
203	VV	15VV1D4509	H4507	BIO-MEDICAL SIGNAL PROCESSING ELECTIVE-III	35	36	1
204	VV	15VV1D4509	H4509	RADAR SIGNAL PROCESSING ELECTIVE-IV	22	38	1
205	VV	15VV1D4509	H4511	ADVANCED SIGNAL PROCESSING LAB	35	52	1
206	VV	15VV1D4509	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURES	33	28	1
207	VV	15VV1D4510	H4501	ADAPTIVE SIGNAL PROCESSING	32	33	1
208	VV	15VV1D4510	H4502	IMAGE & VIDEO PROCESSING	26	31	1
209	VV	15VV1D4510	H4503	WIRELESS COMMUNICATION & NETWORKS	27	40	1
210	VV	15VV1D4510	H4507	BIO-MEDICAL SIGNAL PROCESSING ELECTIVE-III	37	35	1
211	VV	15VV1D4510	H4509	RADAR SIGNAL PROCESSING ELECTIVE-IV	24	39	1
212	VV	15VV1D4510	H4511	ADVANCED SIGNAL PROCESSING LAB	35	52	1
213	VV	15VV1D4510	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURES	35	38	1
214	VV	15VV1D4511	H4501	ADAPTIVE SIGNAL PROCESSING	34	37	1
215	VV	15VV1D4511	H4502	IMAGE & VIDEO PROCESSING	28	29	1
216	VV	15VV1D4511	H4503	WIRELESS COMMUNICATION & NETWORKS	32	46	1
217	VV	15VV1D4511	H4507	BIO-MEDICAL SIGNAL PROCESSING ELECTIVE-III	35	41	1
218	VV	15VV1D4511	H4509	RADAR SIGNAL PROCESSING ELECTIVE-IV	30	42	1
219	VV	15VV1D4511	H4511	ADVANCED SIGNAL PROCESSING LAB	35	47	1
220	VV	15VV1D4511	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURES	34	39	1
221	VV	15VV1D4512	H4501	ADAPTIVE SIGNAL PROCESSING	32	32	1
222	VV	15VV1D4512	H4502	IMAGE & VIDEO PROCESSING	27	24	1
223	VV	15VV1D4512	H4503	WIRELESS COMMUNICATION & NETWORKS	32	40	1
224	VV	15VV1D4512	H4507	BIO-MEDICAL SIGNAL PROCESSING ELECTIVE-III	38	25	1
225	VV	15VV1D4512	H4509	RADAR SIGNAL PROCESSING ELECTIVE-IV	29	36	1
226	VV	15VV1D4512	H4511	ADVANCED SIGNAL PROCESSING LAB	32	42	1
227	VV	15VV1D4512	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURES	33	28	1
228	VV	15VV1D4513	H4501	ADAPTIVE SIGNAL PROCESSING	34	41	1
229	VV	15VV1D4513	H4502	IMAGE & VIDEO PROCESSING	29	24	1
230	VV	15VV1D4513	H4503	WIRELESS COMMUNICATION & NETWORKS	26	30	1
231	VV	15VV1D4513	H4507	BIO-MEDICAL SIGNAL PROCESSING ELECTIVE-III	37	32	1
232	VV	15VV1D4513	H4509	RADAR SIGNAL PROCESSING ELECTIVE-IV	27	32	1
233	VV	15VV1D4513	H4511	ADVANCED SIGNAL PROCESSING LAB	32	44	1
234	VV	15VV1D4513	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURES	33	30	1

235	VV	15VV1D4514	H4501	ADAPTIVE SIGNAL PROCESSING	36	38	1
236	VV	15VV1D4514	H4502	IMAGE & VIDEO PROCESSING	32	29	1
237	VV	15VV1D4514	H4503	WIRELESS COMMUNICATION & NETWORKS	37	36	1
238	VV	15VV1D4514	H4507	BIO-MEDICAL SIGNAL PROCESSING ELECTIVE-III	37	42	1
239	VV	15VV1D4514	H4509	RADAR SIGNAL PROCESSING ELECTIVE-IV	29	41	1
240	VV	15VV1D4514	H4511	ADVANCED SIGNAL PROCESSING LAB	33	44	1
241	VV	15VV1D4514	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURES	35	33	1
242	VV	15VV1D4515	H4501	ADAPTIVE SIGNAL PROCESSING	30	31	1
243	VV	15VV1D4515	H4502	IMAGE & VIDEO PROCESSING	27	24	1
244	VV	15VV1D4515	H4503	WIRELESS COMMUNICATION & NETWORKS	26	28	1
245	VV	15VV1D4515	H4507	BIO-MEDICAL SIGNAL PROCESSING ELECTIVE-III	37	30	1
246	VV	15VV1D4515	H4509	RADAR SIGNAL PROCESSING ELECTIVE-IV	24	33	1
247	VV	15VV1D4515	H4511	ADVANCED SIGNAL PROCESSING LAB	32	42	1
248	VV	15VV1D4515	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURES	32	28	1
249	VV	15VV1D4516	H4501	ADAPTIVE SIGNAL PROCESSING	32	32	1
250	VV	15VV1D4516	H4502	IMAGE & VIDEO PROCESSING	30	24	1
251	VV	15VV1D4516	H4503	WIRELESS COMMUNICATION & NETWORKS	30	36	1
252	VV	15VV1D4516	H4507	BIO-MEDICAL SIGNAL PROCESSING ELECTIVE-III	35	28	1
253	VV	15VV1D4516	H4509	RADAR SIGNAL PROCESSING ELECTIVE-IV	26	39	1
254	VV	15VV1D4516	H4511	ADVANCED SIGNAL PROCESSING LAB	33	43	1
255	VV	15VV1D4516	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURES	36	28	1
256	VV	15VV1D4517	H4501	ADAPTIVE SIGNAL PROCESSING	33	36	1
257	VV	15VV1D4517	H4502	IMAGE & VIDEO PROCESSING	28	27	1
258	VV	15VV1D4517	H4503	WIRELESS COMMUNICATION & NETWORKS	32	42	1
259	VV	15VV1D4517	H4507	BIO-MEDICAL SIGNAL PROCESSING ELECTIVE-III	35	36	1
260	VV	15VV1D4517	H4509	RADAR SIGNAL PROCESSING ELECTIVE-IV	33	43	1
261	VV	15VV1D4517	H4511	ADVANCED SIGNAL PROCESSING LAB	37	53	1
262	VV	15VV1D4517	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURES	32	29	1
263	VV	15VV1D4518	H4501	ADAPTIVE SIGNAL PROCESSING	31	28	1
264	VV	15VV1D4518	H4502	IMAGE & VIDEO PROCESSING	25	25	1
265	VV	15VV1D4518	H4503	WIRELESS COMMUNICATION & NETWORKS	28	33	1
266	VV	15VV1D4518	H4507	BIO-MEDICAL SIGNAL PROCESSING ELECTIVE-III	36	34	1
267	VV	15VV1D4518	H4509	RADAR SIGNAL PROCESSING ELECTIVE-IV	20	30	1
268	VV	15VV1D4518	H4511	ADVANCED SIGNAL PROCESSING LAB	32	43	1

269	VV	15VV1D4518	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURES	33	31	1
270	VV	15VV1D4519	H4501	ADAPTIVE SIGNAL PROCESSING	38	38	1
271	VV	15VV1D4519	H4502	IMAGE & VIDEO PROCESSING	28	30	1
272	VV	15VV1D4519	H4503	WIRELESS COMMUNICATION & NETWORKS	31	48	1
273	VV	15VV1D4519	H4507	BIO-MEDICAL SIGNAL PROCESSING ELECTIVE-III	38	32	1
274	VV	15VV1D4519	H4509	RADAR SIGNAL PROCESSING ELECTIVE-IV	29	41	1
275	VV	15VV1D4519	H4511	ADVANCED SIGNAL PROCESSING LAB	35	48	1
276	VV	15VV1D4519	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURES	35	45	1
277	VV	15VV1D5001	H5601	POWER SYSTEM DYNAMICS AND STABILITY	24	35	1
278	VV	15VV1D5001	H5602	FLEXIBLE AC TRANSMISSION SYSTEMS	29	34	1
279	VV	15VV1D5001	H5603	REAL TIME CONTROL OF POWER SYSTEMS	30	37	1
280	VV	15VV1D5001	H5604	ADVANCED POWER SYSTEM PROTECTION	30	36	1
281	VV	15VV1D5001	H5608	VOLTAGE STABILITY ELECTIVE-III	31	47	1
282	VV	15VV1D5001	H5609	POWER SYSTEM DEREGULATION	28	43	1
283	VV	15VV1D5001	H5613	POWER SYSTEMS LAB	23	46	1
284	VV	15VV1D5003	H5601	POWER SYSTEM DYNAMICS AND STABILITY	19	31	1
285	VV	15VV1D5003	H5602	FLEXIBLE AC TRANSMISSION SYSTEMS	28	42	1
286	VV	15VV1D5003	H5603	REAL TIME CONTROL OF POWER SYSTEMS	30	37	1
287	VV	15VV1D5003	H5604	ADVANCED POWER SYSTEM PROTECTION	30	35	1
288	VV	15VV1D5003	H5608	VOLTAGE STABILITY ELECTIVE-III	25	40	1
289	VV	15VV1D5003	H5609	POWER SYSTEM DEREGULATION	29	44	1
290	VV	15VV1D5003	H5613	POWER SYSTEMS LAB	32	44	1
291	VV	15VV1D5004	H5601	POWER SYSTEM DYNAMICS AND STABILITY	19	35	1
292	VV	15VV1D5004	H5602	FLEXIBLE AC TRANSMISSION SYSTEMS	36	50	1
293	VV	15VV1D5004	H5603	REAL TIME CONTROL OF POWER SYSTEMS	34	44	1
294	VV	15VV1D5004	H5604	ADVANCED POWER SYSTEM PROTECTION	37	42	1
295	VV	15VV1D5004	H5608	VOLTAGE STABILITY ELECTIVE-III	31	47	1
296	VV	15VV1D5004	H5609	POWER SYSTEM DEREGULATION	31	52	1
297	VV	15VV1D5004	H5613	POWER SYSTEMS LAB	32	37	1
298	VV	15VV1D5005	H5601	POWER SYSTEM DYNAMICS AND STABILITY	29	35	1
299	VV	15VV1D5005	H5602	FLEXIBLE AC TRANSMISSION SYSTEMS	34	39	1
300	VV	15VV1D5005	H5603	REAL TIME CONTROL OF POWER SYSTEMS	39	43	1
301	VV	15VV1D5005	H5604	ADVANCED POWER SYSTEM PROTECTION	34	33	1
302	VV	15VV1D5005	H5608	VOLTAGE STABILITY ELECTIVE-III	27	44	1

303	VV	15VV1D5005	H5609	POWER SYSTEM DEREGULATION	28	50	1
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306	VV	15VV1D5006	H5602	FLEXIBLE AC TRANSMISSION SYSTEMS	31	44	1
307	VV	15VV1D5006	H5603	REAL TIME CONTROL OF POWER SYSTEMS	35	39	1
308	VV	15VV1D5006	H5604	ADVANCED POWER SYSTEM PROTECTION	38	33	1
309	VV	15VV1D5006	H5608	VOLTAGE STABILITY ELECTIVE-III	30	47	1
310	VV	15VV1D5006	H5609	POWER SYSTEM DEREGULATION	32	50	1
311	VV	15VV1D5006	H5613	POWER SYSTEMS LAB	31	45	1
312	VV	15VV1D5007	H5601	POWER SYSTEM DYNAMICS AND STABILITY	31	37	1
313	VV	15VV1D5007	H5602	FLEXIBLE AC TRANSMISSION SYSTEMS	36	40	1
314	VV	15VV1D5007	H5603	REAL TIME CONTROL OF POWER SYSTEMS	38	46	1
315	VV	15VV1D5007	H5604	ADVANCED POWER SYSTEM PROTECTION	36	35	1
316	VV	15VV1D5007	H5608	VOLTAGE STABILITY ELECTIVE-III	32	47	1
317	VV	15VV1D5007	H5609	POWER SYSTEM DEREGULATION	32	52	1
318	VV	15VV1D5007	H5613	POWER SYSTEMS LAB	31	48	1
319	VV	15VV1D5008	H5601	POWER SYSTEM DYNAMICS AND STABILITY	21	35	1
320	VV	15VV1D5008	H5602	FLEXIBLE AC TRANSMISSION SYSTEMS	32	46	1
321	VV	15VV1D5008	H5603	REAL TIME CONTROL OF POWER SYSTEMS	33	41	1
322	VV	15VV1D5008	H5604	ADVANCED POWER SYSTEM PROTECTION	30	32	1
323	VV	15VV1D5008	H5608	VOLTAGE STABILITY ELECTIVE-III	29	39	1
324	VV	15VV1D5008	H5609	POWER SYSTEM DEREGULATION	28	51	1
325	VV	15VV1D5008	H5613	POWER SYSTEMS LAB	31	35	1
326	VV	15VV1D5009	H5601	POWER SYSTEM DYNAMICS AND STABILITY	27	45	1
327	VV	15VV1D5009	H5602	FLEXIBLE AC TRANSMISSION SYSTEMS	30	50	1
328	VV	15VV1D5009	H5603	REAL TIME CONTROL OF POWER SYSTEMS	38	48	1
329	VV	15VV1D5009	H5604	ADVANCED POWER SYSTEM PROTECTION	34	39	1
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331	VV	15VV1D5009	H5609	POWER SYSTEM DEREGULATION	29	51	1
332	VV	15VV1D5009	H5613	POWER SYSTEMS LAB	35	46	1
333	VV	15VV1D5010	H5601	POWER SYSTEM DYNAMICS AND STABILITY	22	29	1
334	VV	15VV1D5010	H5602	FLEXIBLE AC TRANSMISSION SYSTEMS	31	34	1
335	VV	15VV1D5010	H5603	REAL TIME CONTROL OF POWER SYSTEMS	30	41	1
336	VV	15VV1D5010	H5604	ADVANCED POWER SYSTEM PROTECTION	31	29	1

337	VV	15VV1D5010	H5608	VOLTAGE STABILITY ELECTIVE-III	30	40	1
338	VV	15VV1D5010	H5609	POWER SYSTEM DEREGULATION	28	40	1
339	VV	15VV1D5010	H5613	POWER SYSTEMS LAB	32	39	1
340	VV	15VV1D5012	H5601	POWER SYSTEM DYNAMICS AND STABILITY	21	31	1
341	VV	15VV1D5012	H5602	FLEXIBLE AC TRANSMISSION SYSTEMS	28	41	1
342	VV	15VV1D5012	H5603	REAL TIME CONTROL OF POWER SYSTEMS	33	33	1
343	VV	15VV1D5012	H5604	ADVANCED POWER SYSTEM PROTECTION	28	29	1
344	VV	15VV1D5012	H5608	VOLTAGE STABILITY ELECTIVE-III	29	40	1
345	VV	15VV1D5012	H5609	POWER SYSTEM DEREGULATION	27	48	1
346	VV	15VV1D5012	H5613	POWER SYSTEMS LAB	30	44	1
347	VV	15VV1D5013	H5601	POWER SYSTEM DYNAMICS AND STABILITY	26	41	1
348	VV	15VV1D5013	H5602	FLEXIBLE AC TRANSMISSION SYSTEMS	34	49	1
349	VV	15VV1D5013	H5603	REAL TIME CONTROL OF POWER SYSTEMS	38	44	1
350	VV	15VV1D5013	H5604	ADVANCED POWER SYSTEM PROTECTION	34	33	1
351	VV	15VV1D5013	H5608	VOLTAGE STABILITY ELECTIVE-III	31	49	1
352	VV	15VV1D5013	H5609	POWER SYSTEM DEREGULATION	31	51	1
353	VV	15VV1D5013	H5613	POWER SYSTEMS LAB	25	40	1
354	VV	15VV1D5014	H5601	POWER SYSTEM DYNAMICS AND STABILITY	17	33	1
355	VV	15VV1D5014	H5602	FLEXIBLE AC TRANSMISSION SYSTEMS	29	40	1
356	VV	15VV1D5014	H5603	REAL TIME CONTROL OF POWER SYSTEMS	36	42	1
357	VV	15VV1D5014	H5604	ADVANCED POWER SYSTEM PROTECTION	26	25	1
358	VV	15VV1D5014	H5608	VOLTAGE STABILITY ELECTIVE-III	28	41	1
359	VV	15VV1D5014	H5609	POWER SYSTEM DEREGULATION	29	47	1
360	VV	15VV1D5014	H5613	POWER SYSTEMS LAB	31	41	1
361	VV	15VV1D5015	H5601	POWER SYSTEM DYNAMICS AND STABILITY	21	40	1
362	VV	15VV1D5015	H5602	FLEXIBLE AC TRANSMISSION SYSTEMS	32	38	1
363	VV	15VV1D5015	H5603	REAL TIME CONTROL OF POWER SYSTEMS	34	40	1
364	VV	15VV1D5015	H5604	ADVANCED POWER SYSTEM PROTECTION	27	38	1
365	VV	15VV1D5015	H5608	VOLTAGE STABILITY ELECTIVE-III	30	42	1
366	VV	15VV1D5015	H5609	POWER SYSTEM DEREGULATION	25	50	1
367	VV	15VV1D5015	H5613	POWER SYSTEMS LAB	33	29	1
368	VV	15VV1D5016	H5601	POWER SYSTEM DYNAMICS AND STABILITY	10	-1	0
369	VV	15VV1D5016	H5602	FLEXIBLE AC TRANSMISSION SYSTEMS	8	-1	0
370	VV	15VV1D5016	H5603	REAL TIME CONTROL OF POWER SYSTEMS	0	0	0

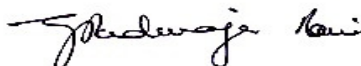
371	VV	15VV1D5016	H5604	ADVANCED POWER SYSTEM PROTECTION	7	2	0
372	VV	15VV1D5016	H5608	VOLTAGE STABILITY ELECTIVE-III	9	-1	0
373	VV	15VV1D5016	H5609	POWER SYSTEM DEREGULATION	10	1	0
374	VV	15VV1D5016	H5613	POWER SYSTEMS LAB	29	-1	0
375	VV	15VV1D5017	H5601	POWER SYSTEM DYNAMICS AND STABILITY	12	28	0
376	VV	15VV1D5017	H5602	FLEXIBLE AC TRANSMISSION SYSTEMS	22	30	1
377	VV	15VV1D5017	H5603	REAL TIME CONTROL OF POWER SYSTEMS	18	32	1
378	VV	15VV1D5017	H5604	ADVANCED POWER SYSTEM PROTECTION	25	25	1
379	VV	15VV1D5017	H5608	VOLTAGE STABILITY ELECTIVE-III	23	38	1
380	VV	15VV1D5017	H5609	POWER SYSTEM DEREGULATION	24	41	1
381	VV	15VV1D5017	H5613	POWER SYSTEMS LAB	27	34	1
382	VV	15VV1D5801	H0501	DATA WAREHOUSING AND DATA MINING	24	42	1
383	VV	15VV1D5801	H2508	CLOUD COMPUTING	39	42	1
384	VV	15VV1D5801	H4002	INFORMATION SECURITY	19	46	1
385	VV	15VV1D5801	H5801	COMPUTER NETWORKS	19	43	1
386	VV	15VV1D5801	H5803	COMPILER DESIGN ELECTIVE-I	14	40	1
387	VV	15VV1D5801	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELECTIVE-I	38	39	1
388	VV	15VV1D5801	H5806	CSE LAB-2	38	53	1
389	VV	15VV1D5802	H0501	DATA WAREHOUSING AND DATA MINING	19	34	1
390	VV	15VV1D5802	H2508	CLOUD COMPUTING	34	37	1
391	VV	15VV1D5802	H4002	INFORMATION SECURITY	18	39	1
392	VV	15VV1D5802	H5801	COMPUTER NETWORKS	14	41	1
393	VV	15VV1D5802	H5803	COMPILER DESIGN ELECTIVE-I	16	34	1
394	VV	15VV1D5802	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELECTIVE-I	31	35	1
395	VV	15VV1D5802	H5806	CSE LAB-2	33	42	1
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399	VV	15VV1D5803	H5801	COMPUTER NETWORKS	17	42	1
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401	VV	15VV1D5803	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELECTIVE-I	39	43	1
402	VV	15VV1D5803	H5806	CSE LAB-2	38	53	1
403	VV	15VV1D5804	H0501	DATA WAREHOUSING AND DATA MINING	24	38	1
404	VV	15VV1D5804	H2508	CLOUD COMPUTING	35	41	1

405	VV	15VV1D5804	H4002	INFORMATION SECURITY	25	43	1
406	VV	15VV1D5804	H5801	COMPUTER NETWORKS	17	42	1
407	VV	15VV1D5804	H5803	COMPILER DESIGN ELECTIVE-I	19	41	1
408	VV	15VV1D5804	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELECTIVE-I	37	38	1
409	VV	15VV1D5804	H5806	CSE LAB-2	38	56	1
410	VV	15VV1D5805	H0501	DATA WAREHOUSING AND DATA MINING	22	28	1
411	VV	15VV1D5805	H2508	CLOUD COMPUTING	33	36	1
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413	VV	15VV1D5805	H5801	COMPUTER NETWORKS	16	34	1
414	VV	15VV1D5805	H5803	COMPILER DESIGN ELECTIVE-I	12	31	0
415	VV	15VV1D5805	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELECTIVE-I	33	33	1
416	VV	15VV1D5805	H5806	CSE LAB-2	35	44	1
417	VV	15VV1D5806	H0501	DATA WAREHOUSING AND DATA MINING	28	36	1
418	VV	15VV1D5806	H2508	CLOUD COMPUTING	36	39	1
419	VV	15VV1D5806	H4002	INFORMATION SECURITY	21	44	1
420	VV	15VV1D5806	H5801	COMPUTER NETWORKS	22	36	1
421	VV	15VV1D5806	H5803	COMPILER DESIGN ELECTIVE-I	23	29	1
422	VV	15VV1D5806	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELECTIVE-I	35	40	1
423	VV	15VV1D5806	H5806	CSE LAB-2	30	41	1
424	VV	15VV1D5807	H0501	DATA WAREHOUSING AND DATA MINING	25	27	1
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427	VV	15VV1D5807	H5801	COMPUTER NETWORKS	18	32	1
428	VV	15VV1D5807	H5803	COMPILER DESIGN ELECTIVE-I	14	31	0
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430	VV	15VV1D5807	H5806	CSE LAB-2	35	57	1
431	VV	15VV1D5808	H0501	DATA WAREHOUSING AND DATA MINING	21	29	1
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437	VV	15VV1D5808	H5806	CSE LAB-2	30	43	1
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441	VV	15VV1D5809	H5801	COMPUTER NETWORKS	19	38	1
442	VV	15VV1D5809	H5803	COMPILER DESIGN ELECTIVE-I	26	46	1
443	VV	15VV1D5809	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELECTI	38	45	1
444	VV	15VV1D5809	H5806	CSE LAB-2	37	52	1
445	VV	15VV1D5810	H0501	DATA WAREHOUSING AND DATA MINING	19	31	1
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448	VV	15VV1D5810	H5801	COMPUTER NETWORKS	13	37	1
449	VV	15VV1D5810	H5803	COMPILER DESIGN ELECTIVE-I	18	36	1
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451	VV	15VV1D5810	H5806	CSE LAB-2	32	41	1
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455	VV	15VV1D5811	H5801	COMPUTER NETWORKS	21	42	1
456	VV	15VV1D5811	H5803	COMPILER DESIGN ELECTIVE-I	26	46	1
457	VV	15VV1D5811	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELECTI	39	40	1
458	VV	15VV1D5811	H5806	CSE LAB-2	39	52	1
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463	VV	15VV1D5812	H5803	COMPILER DESIGN ELECTIVE-I	19	38	1
464	VV	15VV1D5812	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELECTI	36	36	1
465	VV	15VV1D5812	H5806	CSE LAB-2	36	52	1
466	VV	15VV1D5813	H0501	DATA WAREHOUSING AND DATA MINING	17	33	1
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469	VV	15VV1D5813	H5801	COMPUTER NETWORKS	12	34	0
470	VV	15VV1D5813	H5803	COMPILER DESIGN ELECTIVE-I	12	38	1
471	VV	15VV1D5813	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELECTI	33	29	1
472	VV	15VV1D5813	H5806	CSE LAB-2	30	36	1

473	VV	15VV1D5814	H0501	DATA WAREHOUSING AND DATA MINING	32	44	1
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475	VV	15VV1D5814	H4002	INFORMATION SECURITY	29	52	1
476	VV	15VV1D5814	H5801	COMPUTER NETWORKS	23	34	1
477	VV	15VV1D5814	H5803	COMPILER DESIGN ELECTIVE-I	24	49	1
478	VV	15VV1D5814	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELECTIVE-I	39	44	1
479	VV	15VV1D5814	H5806	CSE LAB-2	39	55	1
480	VV	15VV1D5815	H0501	DATA WAREHOUSING AND DATA MINING	23	36	1
481	VV	15VV1D5815	H2508	CLOUD COMPUTING	35	41	1
482	VV	15VV1D5815	H4002	INFORMATION SECURITY	16	42	1
483	VV	15VV1D5815	H5801	COMPUTER NETWORKS	17	34	1
484	VV	15VV1D5815	H5803	COMPILER DESIGN ELECTIVE-I	15	38	1
485	VV	15VV1D5815	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELECTIVE-I	33	36	1
486	VV	15VV1D5815	H5806	CSE LAB-2	38	50	1
487	VV	15VV1D5816	H0501	DATA WAREHOUSING AND DATA MINING	29	43	1
488	VV	15VV1D5816	H2508	CLOUD COMPUTING	36	45	1
489	VV	15VV1D5816	H4002	INFORMATION SECURITY	27	46	1
490	VV	15VV1D5816	H5801	COMPUTER NETWORKS	20	39	1
491	VV	15VV1D5816	H5803	COMPILER DESIGN ELECTIVE-I	23	38	1
492	VV	15VV1D5816	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELECTIVE-I	36	42	1
493	VV	15VV1D5816	H5806	CSE LAB-2	39	57	1

DATE 31-12-2016


CONTROLLER OF EXAMINATIONS(PG)